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# DATASHEET AS55H-010KG-00

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### **History of Revision**

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0. 011 1110011400
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#### 1. OVERVIEW



#### 1.1 Features

- High resolution module, 1.5 Pa
- Supply voltage: 1.8V to 5.5V
- Operating range:0 to 10kPa, -40 to +85  $^{\circ}$ C
- Integrated digital pressure sensor (24-bit ADC)
- Excellent long-term stability
- Standby current<0.2μA @ 25°C
- No external components (Internal oscillator)
- High-Speed I2C and SPI output is in selection
- Size: 4.5 x 4.5 x 3.7 mm

#### 1.2 Applications

- Nature Gas Pressure Measurement
- Adventure and Sports Equipment
- Pipe Pressure measurement
- Water Proof Pressure measurement

#### 1.3 Descriptions

The AS55H-010KG-00 employs a MEMS pressure sensor with an I<sup>2</sup>C interface to provide accurate temperature, pressure data. The sensor pressure and temperature outputs are digitized by a high resolution 24-bit ADC. Data compensation is integrated internally to save the effort of the external host MCU system. Pressure output can be resolved with output in fractions of Pascal. Package is surface mount with a plastic cap and is RoHS compliant.

Sensor Performances (VDD = 3.3 V)								
Pressure	Min	Тур.	Max	Unit				
Range	0		10	kPa				
ADC		24		bit				
Resolution		Pa						
Accuracy@25°C(0 to10 kPa)	-0.1		+0.1	kPa				
Accuracy@-25°C to 50°C (0 to 10kPa)	-0.15		+0.15	kPa				
Response time@ OSR=1024		3.0*2		ms				
Long term stability		±0.1		kPa/yr				



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#### 1.4 Block Diagram

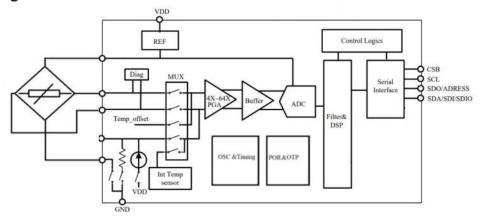


Figure 1: Functional Block Diagram

### 2. Electrical Specifications

#### 2.1 Pressure and Temperature Characteristics

Table1: Pressure Output Characteristics @ VDD = 3.3V, T = 25°C unless otherwise noted

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Operating Pressure Range	P <sub>FS</sub>	Full Accuracy	0		10	kPa
Extended Pressure Range	P <sub>ext</sub>	Linear Range of ADC	0		16	kPa
Absolute Accuracy		at 25°C, 0 to 10 kPa	-0.1		+0.1	kPa
Absolute Accuracy		at -2560°C, 010 kPa	-0.15		+0.15	kPa
Resolution RMS				0.03		kPa
Long Term Drift		After a period of 1 year		±0.1		kPa/yr

- (1) The long-term stability is measured with non-soldered devices.
- (2) Reflow soldering impact ±2.5 mbar.
- (3) Recovering time after reflow (Time to recover at least 80% of the reflow impact) 5 days.

Table2: Temperature Output Characteristics @ VDD = 3.3V, T = 25°C unless otherwise noted

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Operation Temperature Range	ТОР		-40	25	85	$^{\circ}$
Temperature Absolute Accuracy		25℃		0.5	+0.8	$^{\circ}$
Temperature Absolute Accuracy		-25℃ to +60℃		1		$^{\circ}$
Temperature Resolution of Output Data				0.01		$^{\circ}$



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#### 2.2 Electrical Characteristics

Table3: DC Characteristics @VDD=3.3 V, T=25 ℃ unless otherwise noted

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Operation Supply Voltage	V <sub>DD</sub>		1.8	3.3	5.5	V
Operation Temperature	ТОР		-40		85	℃
Supply Current @25°C on during conversion	I <sub>BDD</sub> _pga on	PGA on (Gain>=4)		1.8		mA
Conversion time	Тс	OSR 32768 16384 8192 4096 2048 1024 512 256		43.0 35.0 12.0 7.0 4.0 3.0 2.0 2.0		ms
Supply current (1 sample per sec.)	ldd	OSR 32768 16384 8192 4096 2048 1024 512 256		77.4 63.0 21.6 12.6 7.2 5.4 3.6 3.6		uA
Standby Supply Current	IDDSTB	At25℃		0.1	0.2	μΑ
Serial Data Clock Frequency	fSCLK	I <sup>2</sup> C protocol, pull- up resistor of 4k7~10k		100	400	kHz
Digital Input High Voltage	VIH		0.8			V
Digital Input Low Voltage	VIL				0.2	V
Digital Output High Voltage	Voн	IO=0.5mA	0.9			V
Digital Output Low Voltage	VOL	IO=0.5mA			0.1	V
Input Capacitance	CIN			4.7		pF

<sup>(1)</sup> Standard Product. Could be customized.



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#### 2.3 Absolute Maximum Rating

Table 4: Absolute Maximum Rating

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Overpressure	РМА				1	bar
Supply Voltage	V <sub>DD</sub>		-0.3		5.5	V
Interface Voltage	VIF		-0.3		VDD+0.3	V
Storage Temperature Range	TSTG		-40		85	$^{\circ}$
Maximum Soldering Temperature	TMS	40 second maximum			250	$^{\circ}$
ESD Rating		Human body model	-2		+2	kV
Latch-up Current		At 85℃	-100		100	mA

Stresses above those listed as "absolute maximum ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device under these conditions is not implied. Exposure to maximum rating conditions for extended periods may affect device reliability.

#### 3. Function Descriptions

#### 3.1 General Description

The AS55H-010KG-00 consists of a piezo-resistive sensor and a sensor interface  $I^2$  C. The main function of the  $I^2$  C is to convert the uncompensated analogue output voltage from the piezo-resistive pressure sensor to a 24-bit digital value, as well as providing a 16-bit digital value for the temperature of the sensor, and compensates them by a patented algorithm. The fully-compensated values can be read out by external MCU.

#### 3.2 Factory Calibration

Every sensor is individually factory calibrated for sensitivity and offset for both of the temperature and pressure measurements, further calibrations are not necessary to be done by the user. The OTP registers are used to store the configurations and calibration coefficients for the sensor.

#### 3.3 Sensor Output Conversion

For each pressure measurement, customer used to send a conversion command to the sensor, read back the conversion data from the normal register to be stored from 0x06 to 0x0a, the pressure data is stored from 0x06 to 0x08, the highest bit is sign bit, the temperature data is stored from 0x09 to 0x0a, the highest bit is sign bit. All the data are sent starting from the MSB.

#### 3.4 Serial Interface

The AS55H-010KG-00 provides both SPI and I<sup>2</sup> C interface for serial communication and 'CSB' pin is used to switch between these two protocols. Pulling 'CSB' pin low selects the SPI interface, leaving 'CSB' pin float or puling it high selects the I<sup>2</sup> C interface.

PIN CSB	MODE	PIN USED	NOTE
Float	I2C	SDA, SCL	
Low	SPI	SDI, SDO, SCK	Default 3SPI



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#### 4. REGISTERS

All the registers can be departed into normal registers and OTP registers. The normal registers are used to send a conversion command to the Sensor, read back the conversion data and perform the OTP blowing. The OTP registers are used to store the configurations and calibration coefficients for the Sensor, whose default values can be programmed by the inside OTP banks.

#### **4.1. NORMAL REGISTERS**

#### Table4.1 normal registers

Addr	Description	R/W	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0	Default
0x00	SPI _Ctrl	RW	SDO_ac	LSB_fi	Softreset			Softres	LSB_firs	SDO_active	0x00
			tive	rst				et			
0x01	Part_ID	R	PartID								0x00
0x02	Status	R	Error_cod	е					1'b0	DRDY	
0x06	DATA_MSB	R	Data out[2	Data out[23:16]							0x00
0x07	DATA_CSB	R	Data out[1	15:8]							0x00
0x08	DATA_LSB	R	Data out[7	7:0]							0x00
0x09	TEMP_MSB	R	Temp out	Temp out[15:8]						0x00	
0x0A	TEMP_LSB	R	Temp out	[7:0]							0x00
0x30	CMD	RW	Sleep_tim	e[3:0]			Sco	Measure	ment_ctrl]2	:0]	0x00

#### Reg0x00

SDO\_active: 1: 4-wire SPI, 0: 3-wire SPI

LSB first: 1: LSB first for SPI interface, 0: MSB first for SPI interface

Soft\_reset: 1: Reset all the NSA2300 registers (except 'margin'), automatically come back to 0 after

reset complete.

#### Reg0x01

PartID: OTP programmed 8 bits Part ID, corresponding to OTP register Reg0xA4. Read only from the address 0x01.

#### Reg0x02

DRDY: 1, indicates once conversion complete, and the output data is ready for reading. Error\_code: When diagnostic function enabled, These bits stores the error information.

Error\_code[3]: VINP short to VDD

Error\_code[2]: VINP short to GND Error\_code[1]: VINN short to VDD

Error\_code[0]: VINN short to GND

#### Reg0x06-Reg0x08

Data\_out: 24 bits ADC output data when 'raw\_data\_on' = 0 with an LSB equals to  $(1/2^23)*(VEXT-PSW)$ . 24 bits calibrated data when 'raw\_data\_on' = 1.

#### Reg0x09-Reg0x0a

Temp\_out: Temperature output with an LSB equals to (1/256) °C

#### Reg0x30

Sleep\_time[3:0]: 0000:0ms, 0001:62.5ms, 0010:125ms ... 1111: 1s, only active during sleep mode conversion.

Measurement\_control: 000b, indicate a single shot temperature signal conversion. 001b, indicate a single shot sensor signal conversion. 010b: indicate a combined conversion (once temperature conversion immediately followed by once sensor signal conversion). 011b: indicate a sleep mode conversion (periodically perform once combined conversion with an interval time of 'sleep\_time'), 100b: OTP programming mode, enter this mode to when programming OTP banks.

Sco: 1, Start of conversion, automatically come back to 0 after conversion ends (except sleep mode conversion).



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#### **4.2** OTP REGISTERS

#### Table4.2 OTP registers

Addr	Description	R/W	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0	Default
0xa4	Part_ID	RW									OTP
0xa5	Sys_config	RW								DIAG_on	ОТР
0xa6	P_config	RW						OSR_P[	2:0]		ОТР
0xa7	T_config	RW						OSR_T[	2:0]		ОТР

#### Reg0xA4

PartID: OTP programmed 8 bits Part ID, also can be read from address 0x01.

#### Reg0xA5

Diag\_on: 1, Enable diagnosis function.

#### Reg0xA6

OSR\_P: set the over sampling ratio of the sensor signal conversion channel. 000:1024X, 001:2048X, 010:4096X, 011:8192X, 100:256X, 101:512X, 110:16384X, 111:32768X.

#### Reg0xA7

OSR\_T: set the over sampling ratio of the temperature conversion channel. 000:1024X, 001:2048X, 010:4096X, 011:8192X, 100:256X, 101:512X, 110:16384X, 111:32768X.

#### 5. High-Speed I<sup>2</sup>C Digital Output Interface

The I<sup>2</sup>C interface is fully compatible to the official I<sup>2</sup>C protocol specification.

#### 5.1 I<sup>2</sup>C Specification

Table5: I<sup>2</sup>C Slave Timing Values

				11.2		
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Clock frequency	f <sub>BsclB</sub>				400	kHz
SCL low pulse	t <sub>BLOWB</sub>		1.3			μs
SCL high pulse	t <sub>внібнв</sub>		0.6			μs
SDA setup time	t <sub>BSUDATB</sub>		0.1			μs
SDA hold time	t <sub>BHDDATB</sub>		0.0			μs
Setup Time for a repeated start condition	t <sub>BSUSTAB</sub>		0.6			μs
Hold time for a start condition	t <sub>BHDSTAB</sub>		0.6			μs
Setup Time for a stop condition	t <sub>вѕиѕтов</sub>		0.6			μs
Time before a new transmission can start	t <sub>BBUFB</sub>		1.3			μs



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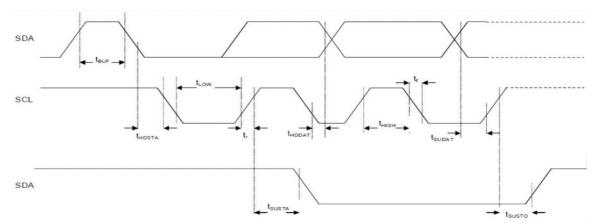


Figure 2: I<sup>2</sup>C Timing Diagram

The I<sup>2</sup>C interface protocol has special bus signal conditions. Start (S), stop (P) and binary data conditions are shown below. At start condition, SCL is high and SDA has a falling edge. Then the slave address is sent. After the 7 address bits, the direction control bit R/W selects the read or write operation. When a slave device recognizes that it is being addressed, it should acknowledge by pulling SDA low in the ninth SCL (ACK) cycle.

At stop condition, SCL is also high, but SDA has a rising edge. Data must be held stable at SDA when SCL is high. Data can change value at SDA only when SCL is low.

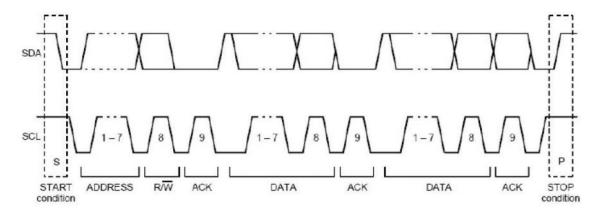


Figure 3: I<sup>2</sup>C Protocol

#### 5.2 I<sup>2</sup>C Device Address

The I<sup>2</sup>C device address is shown below. The LSB of the device address is corresponding to address 0XDA (write) and 0XDB (read).

A7	A6	A5	A4	А3	A2	A1	W/R
1	1	0	1	1	0	SDO/ADDR	0/1



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5.3 I <sup>2</sup> CProt 5.3.1 P_Con														
	From master to slave						A A	A ACK						
	From slave to master					Stop			NACK					
S Slave	Addr 0 A	Regist	er A		_Data	A	P							
Register=0XA						1400								
Config_Data:														
Address	Description	R/W	Bit7	Bit6	Bi	t5 E	3it4	Bit3	Bit2	Bit1	Bit0	default		
0xA6	P_CONFIG	RW			No ch	nange			OSR_P<	:2:0>		ОТР		
OSR P: set t	ne over sampli	ng ratio o	f the ser	l Isor signal	convers	sion ch	hannel	000.	1024X	001:204	8X 010:	1096X		
_	.00:256X, 101:	-		_		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	ilailii Ci	. 000.	10247,	001.204	OA, 010.	+030 <i>N</i> ,		
5.3.2 T_Conf	_			1				ACK						
	naster to slav lave to maste		0	Start										
			Р	Stop			8	NACI	<					
S Slave		Regist	er A	Config	_Data	A	Р							
Register=0XA7														
Config_Data:														
	_	5 /14/	5	D.: C	D::E	5:14	D::0		2112	D'14	D'10	1.6.1		
Address	Description		Bit7	Bit6	Bit5	Bit4	Bit3		Bit2	Bit1	Bit0	default		
Address 0xA7	Description T_CONFIG	RW			No char	nge		OS	R_T<2:0	>		ОТР		
Address  0xA7  OSR_T: set th	Description T_CONFIG ne over sampli	RW ng ratio o	f the ser	nsor signal	No char	nge		OS	R_T<2:0	>		ОТР		
Address  0xA7  OSR_T: set tl 011:8192X, 1	Description T_CONFIG ne over sampli 00:256X, 101:	RW ng ratio o	f the ser	nsor signal	No char	nge		OS	R_T<2:0	>		ОТР		
Address  0xA7  OSR_T: set tl 011:8192X, 1  5.3.3 Send Co	Description T_CONFIG ne over sampli 00:256X, 101:	RW ng ratio o 512X, 110	f the ser	nsor signal	No char	nge	nannel	OS	R_T<2:0	>		ОТР		
Address  0xA7  OSR_T: set tl 011:8192X, 1  5.3.3 Send Co	Description T_CONFIG ne over sampli 00:256X, 101:	RW ng ratio o 512X, 110	f the ser 1: 163842	nsor signal X, 111:327	No char	nge	nannel.	OS . 000:	 R_T<2:03 1024X,	>		ОТР		
Address  0xA7  OSR_T: set tl 011:8192X, 1  5.3.3 Send Co  From r	Description T_CONFIG ne over sampli 00:256X, 101: command master to slav	RW ng ratio o 512X, 110	f the ser 1: 163842 S	nsor signal X, 111:327	No char	nge	nannel.	OS . 000:	 R_T<2:03 1024X,	>		ОТР		
Address  0xA7  OSR_T: set tl 011:8192X, 1  5.3.3 Send Co	Description T_CONFIG ne over sampli 00:256X, 101: command master to slav	RW ng ratio o 512X, 110	f the ser 1: 163842 S	ssor signal X, 111:327 Start	No char convers 68X.	nge sion ch	nannel.	OS . 000:	 R_T<2:03 1024X,	>		ОТР		
Address  0xA7  OSR_T: set tl 011:8192X, 1  5.3.3 Send Co  From r  From s  S Slave  ComReg=0x3	Description T_CONFIG ne over sampli 00:256X, 101: command master to slav slave to master Addr 0 A	RW ng ratio o 512X, 110	f the ser 1: 163842 S	sor signal X, 111:327 Start	No char convers 68X.	nge sion ch	nannel.	OS . 000:	 R_T<2:03 1024X,	>		ОТР		
Address  0xA7  OSR_T: set tl 011:8192X, 1  5.3.3 Send Co From r  From s	Description T_CONFIG ne over sampli 00:256X, 101: command master to slav slave to master Addr 0 A	RW ng ratio o 512X, 110	f the ser 1: 163842 S	sor signal X, 111:327 Start	No char convers 68X.	nge sion ch	nannel.	OS . 000:	 R_T<2:03 1024X,	>		ОТР		
Address  0xA7  OSR_T: set tl 011:8192X, 1  5.3.3 Send Co  From r  From s  S Slave  ComReg=0x3	Description T_CONFIG ne over sampli 00:256X, 101: command master to slav slave to master Addr 0 A	RW ng ratio o 512X, 110	f the ser 1: 163842 S	Start Stop Comma	No char convers 68X.	nge sion ch	nannel.	0S . 000:	 R_T<2:03 1024X,	001:204		ОТР		
Address  0xA7  OSR_T: set tl 011:8192X, 1  5.3.3 Send Co From r  From s  S Slave  ComReg=0x3 CommandDa	Description T_CONFIG  ne over sampli 00:256X, 101:  command  master to slav slave to master Addr 0 A  0 ta:	RW ng ratio o 512X, 110 /e er ComR	f the ser l: 163842 S P Reg A	Start Stop Comma	No char convers	a A	nannel.	os . 000: ACK NAC	R_T<2:0; 1024X,	001:204	8X, 010:-	OTP 4096X,		
Address  0xA7  OSR_T: set tl 011:8192X, 1  5.3.3 Send Co  From s  S Slave  ComReg=0x3 CommandDa  Address  0x30	Description T_CONFIG  ne over sampli 00:256X, 101:  master to slav slave to master Addr 0 A  0 ta: Description	RW ng ratio o 512X, 110 /e er  ComR	f the ser 163842  Seg A  Bit7  Sleep_ti	start Stop Comma Bit6 me<3:0>	No char convers 68X.	a A	nannel.  A N P Bit3 Sco	OS OOO: ACK NAC	R_T<2:02 1024X,  Bit1 urement	001:204	8X, 010:-	OTP 4096X,  default OTP		
Address  0xA7  OSR_T: set tl 011:8192X, 1  5.3.3 Send Co  From r  From s  S Slave  ComReg=0x3 CommandDa  Address  0x30  Sleep_time<	Description T_CONFIG  ne over sampli 00:256X, 101:  master to slav slave to master Addr 0 A  0 ta: Description CMD	RW ng ratio o 512X, 110 //e er  ComR	f the ser 1: 163842  S P Bit7 Sleep_ti	Start Stop Comma Bit6 me<3:0> 10:125ms.	No char convers 68X.  Bit5 E	a A Bit4	Bit3 Sco	OS OOO: ACK NAC Bit2 Meas	R_T<2:02 1024X,  Bit1 urement	001:204	Bit0	default OTP		
Address  0xA7  OSR_T: set tl 011:8192X, 1  5.3.3 Send Co From r From s  S Slave  ComReg=0x3 CommandDa  Address  0x30  Sleep_time< Measurement immediately	Description T_CONFIG ne over sampli 00:256X, 101: command master to slav slave to master Addr 0 A 0 ta: Description CMD 3:0>:0000:0m	RW ng ratio o 512X, 110 //e er  ComR  R/W RW  RW  s, 0001:62 D>: 010b nce senso	Fithe ser  1: 163842  Seg A  Bit7  Sleep_ti  2:5ms,000  : indicator signal of	start Stop Comma Bit6 me<3:0> 10:125ms. ate a conconversion	No char convers 68X.  Bit5 E	a A  Bit4  : 1s, o	A / N P Bit3 Sco only activersion	OS OOO: ACK NAC Bit2 Meas	R_T<2:02 1024X,  Bit1 urement uring sle	O01:204	8X, 010:-	default OTP		



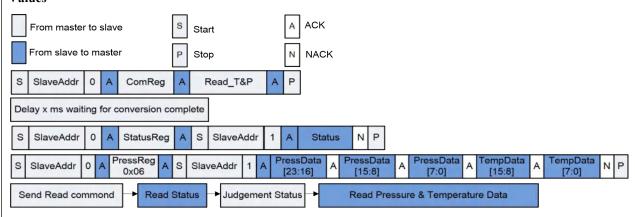
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5.3	3.4	Read Sta	tus															
	From master to slave				S Start				A ACK				C					
		From sla	ave to	o ma	aste	r		Р	Sto	pp			Ν	NA	CK			StatusReg= 0x02
	S	SlaveA	Addr	0	А	Statu	sReg	Α	s	Sla	veAddr	1	А	\$	Status	N	Р	Status:
	Α	ddress	Desc	cripti	on	R/W	Bit7	Bit	t6	Bit5	Bit4	Bit3	E	it2	Bit1	Bit	0	
		0x02	St	atus		R		•	•				1	'b0		DRE	Υ	

**DRDY:** 1, indicates once conversion complete, and the output data is ready for reading.

#### 5.3.5 Read the Pressure & Temperature

#### Values



Pressure ADC bits are 24 bits 2's complement. Data format: the highest bit is the sign bit (0 is a positive number, 1 is a negative number), 23 data bits. In the 23-bit data bits, there are high N integer bits, and the low n bits are decimal bit, read the ADC number and convert it to Pa. The formula is:Pressure = Read\_ADC value/2^n.

Pressure(
$$Pa$$
)=  $(\frac{\text{Read\_PADC}[23:0]}{2^9})$ 

Temperature ADC bits are 16 bits 2's complement. Data format: the highest bit is the sign bit (0 is a positive number, 1 is a negative number), 15 data bits. The high 7 bits are integer bits, the low 8 bits are decimal bits, equals to (1/256) °C.

Temperature (°C) = 
$$\left(\frac{\text{Read\_TADC}[15:0]}{256}\right)$$



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#### 6. SPI INTERFACE

provides both SPI and I2C interface for serial communication and 'CSB' pin is used to switch between these two protocols. Pulling 'CSB' pin low selects the SPI interface, leaving 'CSB' pin float or puling it high selects the I2C interface.

#### **6.1. INTERFACE SPECIFICATION**

Table 6.1 SPI interface specifications

Symbol	Parameter	Condition	Min	Max	Unit
fBsclkB	Clock frequency	Max load on SDIO or SDO = 25pF		10	MHz
tBsclk_IB	SLCK low pulse		20		Ns
tBsclk_hB	SLCK high pulse		20		
TBsdi_setupB	SDI setup time		20		ns
TBsdi_holdB	SDI hold time		20		ns
TBsdo_odB	SDO/SDI output delay	Load = 25pF		30	ns
		Load = 250pF		40	ns
TBcsb_setupB	CSB setup time		20		ns
TBcsb_holdB	CSB hold time		40		ns

The figure below shows the definition of the SPI timing given in table 5.1

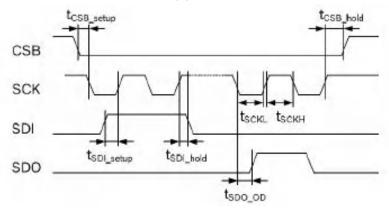


Figure 6.1 SPI timing diagram

The falling edge of CSB, in conjunction with the rising edge of SCLK, determines the start of framing. Once the beginning of the frame has been determined, timing is straightforward. The first phase of the transfer is the instruction phase, which consists of 16 bits followed by data that can be of variable lengths in multiples of 8 bits. If the device is configured with CSB tied low, framing begins with the first rising edge of SCLK. The instruction phase is the first 16 bits transmitted. As shown in Figure 5.2, the instruction phase is divided into a number of bit fields.

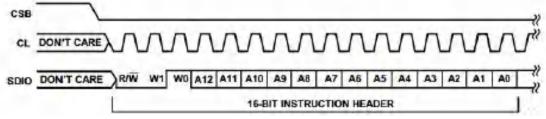


Figure 6.2, Instruction Phase Bit Field.



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The first bit in the stream is the read/write indicator bit (R/W). When this bit is high, a read is being requested, otherwise indicates it is a write operation. W1 and W0 represent the number of data bytes to transfer for either read or write (Table 5.2). If the number of bytes to transfer is three or less (00, 01, or 10), CSB can stall high on byte boundaries. Stalling on a nonbyte boundary terminates the communications cycle. If these bits are 11, data can be transferred until CSB transitions high.

CSB is not allowed to stall during the streaming process. The remaining 13 bits represent the starting address of the data sent. If more than one word is being sent, sequential addressing is used, starting with the one specified, and it either increments (LSB first) or decrements (MSB first) based on the mode setting.

Table 6.2. W1 and W0 settings

W1:W0	Action	CSB stalling
00	1 byte of data can be transferred.	Optional
01	2 bytes of data can be transferred.	Optional
10	3 bytes of data can be transferred.	Optional
11	4 or more bytes of data can be transferred. CSB must be held low	No
	for entire sequence; otherwise, the cycle is terminated.	

Table6.2. W1 and W0 settings

Data follows the instruction phase. The amount of data sent is determined by the word length (Bit W0 and Bit W1). This can be one or more bytes of data. All data is composed of 8-bit words. Data can be sent in either MSB-first mode or LSB-first mode (by setting 'LSB first' bit). On power up, MSB-first mode is the default. This can be changed by programming the configuration register. In MSB-first mode, the serial exchange starts with the highest-order bit and ends with the LSB. In LSB-first mode, the order is reversed. (Figure 5.3)

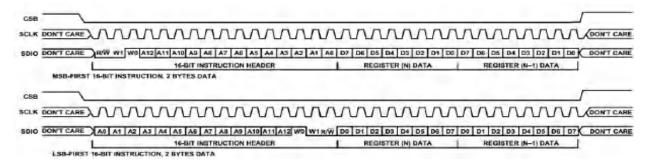


Figure 6.3: MSB First and LSB First Instruction and Data Phases

Register bit 'SDO\_active' is responsible for activating SDO on devices. If this bit is cleared, then SDO is inactive and read data is routed to the SDIO pin. If this bit is set, read data is placed on the SDO pin. The default for this bit is low, making SDO inactive.

#### 6.2. SPI Protocol



#### 6.2.1 SPI Write one byte

CMD:0X00

6.2.2 SPI Read one byte

CMD:0X80

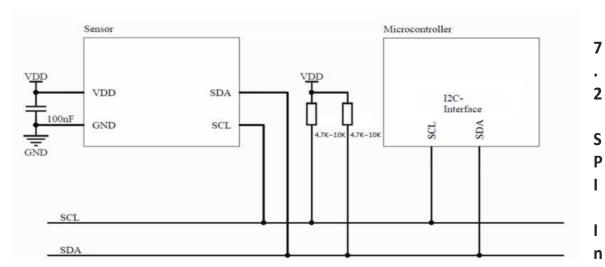
#### 6.2.3 SPI Read Sensor (Pressure and Temperature)

Send Read cmd (0x00)(0x30)(0x0A)	WaitADC	Read 0x06	Read 0x07	Read 0x08	Read 0x09	Read 0x0A
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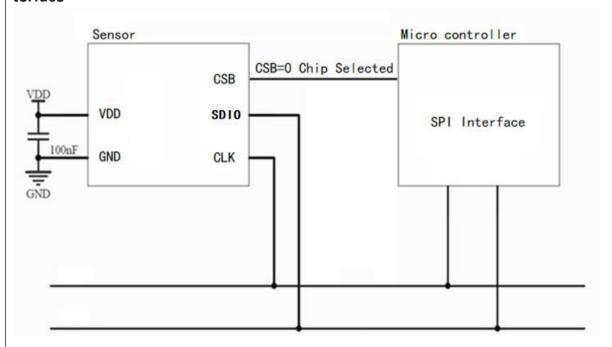
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### 7. Application Circuit

### 7.1 I<sup>2</sup>C Interface



#### terface





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### 8. Package Outline and Pin Configuration (unit: mm)

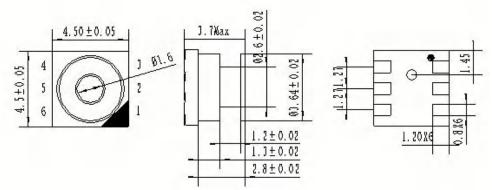
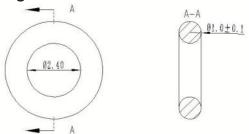


Figure 5: Package Outlines

			1 Bare 311 denage damines
Pin	Name	Туре	Function
1	VDD	Р	Positive supply voltage
2	ADR/SD0	1/0	Serial data output in 4-wire SPI mode
			Address select in I2C mode
3	SCL	I	Serial data clock
4	SDA/SDI/SDIO	1/0	Serial data input/output in I2C mode (SDA)
			Serial data input in 4-wire SPI mode (SDI)
			Serial data input/output in 3-wire SPI mode (SDIO)
5	CSB	I	Chip select (Float I <sup>2</sup> C / Low SPI)
6	GND	G	Power Ground

Figure 6: Pin Configuration and Description

### 8.1 Recommend to the "O" ring select



Note: Silicon rubber ,hardness 50  $\pm$ 5 shore A

### 9. Recommended Pad Layout (unit: mm)

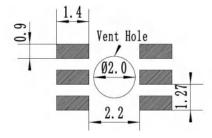
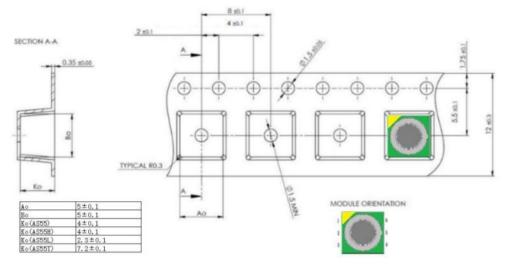


Figure 7: Pad layout

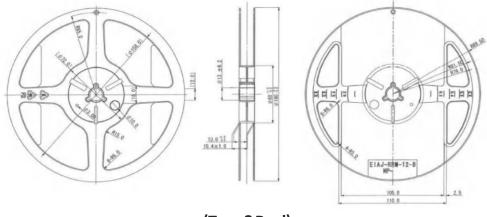


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### 10. Shipping Package



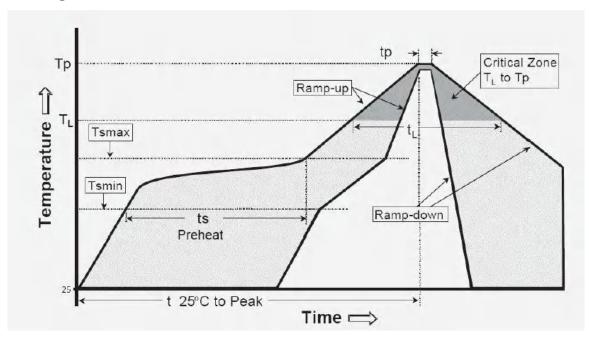
ITEM	W	A0	В0	K0	K1	P1	F	E	D	D1	PO	P2	T
											4.0		
TOLE	±0.3	±0.1	±0.1	±0.1	±0.1	±0.1	±0.05	±0.1	+0.1	min	±0.1	±0.1	±0.05



(Tape&Reel)

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### 11. Soldering Recommendation (IPC/JEDEC J-STD-020D)



IPC/JEDEC J-STD-020D	Pb-Free Assembly
Average Ramp-up rate(TL-Tp)	3℃/s(Max.)
Preheat	
-Temperature Min. (Tsmin)	140℃
-Temperature Max. (Tsmax)	170℃
–Time (Min. to Max. ) (ts)	60-180 seconds
Tsmax to TL –Tp	3℃/s (Max.)
Time maintained above:	
-Temperature (TL)	200°C above
–Time (tL)	60-150 seconds
Peak temperature (Tp)	200~220℃
Time of Real peak temperature within 5 °C (tp)	40 seconds
Average Ramp-down rate(Tp-TL)	6℃/s (Max.)
Time 25 ℃ to peak temperature	8min. (Max.)

#### Note

- 1) It is recommended that only one time reflow soldering, no more than two times.
- 2) After reflow soldering or other high temperature processes, wait for at least 48 hours (or as required by the data sheet) before data reading and processing.
- 3) Spot cleaning by hand if necessary, DO NOT wash or submerge sensor in cleaning liquid.
- 4) It is recommended to use the medium temperature solder paste.



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#### 12. LEGAL DISCLAIMER

- 1) For the export of products which are controlled items subject to foreign and domestic export laws and regulations, you must obtain approval and/or follow the formalities of such laws and regulations.
- 2) Products must not be used for military and/or antisocial purposes such as terrorism, and shall not be supplied to any party intending to use the products for such purposes.
- 3) Unless provided otherwise, the products have been designed and manufactured for application to equipment and devices which are sold to end-users in the market.
- 4) Before using products which were not specifically designed for use in automotive applications, please contact an AIOT sales representative.
- 5) This specification is subject to change without notice.

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